



Patent
Docket No.: 55271US002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

John D. Geissinger, Paul M. Harvey, and
Robert R. Kieschke

Serial No.: 09/491,302

Filed: January 25, 2000

For: ELECTRONIC PACKAGE WITH
INTEGRATED CAPACITOR

Group Art Unit: 2815

Examiner: Paul E. Brock II

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10/19/12
Sunder

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AMENDMENT AND RESPONSE

Commissioner for Patents
Washington, DC 20231

Dear Sir:

This is an amendment in response to the Official Action of April 26, 2002. Please enter the amendments and consider the remarks below.

IN THE CLAIMS:

Please amend claim 1 as follows:

Claim 1 (Twice Amended). An electronic package, comprising:

- a conductive trace layer having a first side and a second side, the conductive trace layer being patterned to define a plurality of interconnect pads;
- a dielectric substrate mounted on the first side of the conductive trace layer made of a non-conductive polymer blended with high dielectric constant particles;
- a capacitor having a capacitance of from about 1 nF/sq.cm. to about 100 nF/sq.cm. including a first conductive layer, a second conductive layer and a layer of dielectric material disposed between the first and the second conductive layers, the first conductive layer mounted adjacent to the second side of the conductive trace layer;

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